

Patent claims

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1. A polymer mixture, the polymer mixture having semiconductive properties and
  - one or more semiconductive polymers,
  - one or more non-semiconductive polymersbeing present in the polymer mixture.
2. The polymer mixture as claimed in claim 1, characterized in that the semiconductive polymer/the semiconductive polymers is/are polythiophene, polyfluorene and/or polythienylenevinylene.
3. The polymer mixture as claimed in either of the preceding claims, characterized in that the non-semiconductive polymer/the non-semiconductive polymers is/are polystyrene, polymethyl methacrylate, cymel and/or polyisobutyl.
4. The polymer mixture as claimed in any of the preceding claims, characterized in that it contains solvents, in particular chloroform, toluene, ketones, dioxane and/or heptane.
5. The polymer mixture as claimed in any of the preceding claims, characterized in that it additionally contains molecules which are smaller than polymers, in particular oligomers, conductive molecules and/or semiconductive molecules.
6. The polymer mixture as claimed in any of the preceding claims, characterized in that it consists of said substances and customary additives.

- 9 -

7. The polymer mixture as claimed in any of the preceding claims, characterized in that it has a viscosity of more than 8 mPa.s, in particular of more than 80 mPa.s.
8. A printing process for the production of a semiconductive double layer by a known process, such as the screen printing, flexographic printing, offset printing, gravure printing and/or pad printing process, a polymer mixture as claimed in any of the preceding claims being used as print medium.
9. A printing process for the production of a semiconductive double layer by a known process, such as the screen printing, flexographic printing, offset printing, gravure printing and/or pad printing process, the double layer produced by printing the printing medium containing
  - one or more semiconductive polymers in one of its layers,
  - one or more non-semiconductive polymers in its other layer.
10. The printing process for the production of a double layer as claimed in claim 9, in which a polymer mixture as claimed in any of claims 1 to 7 is used.
11. An electronic component, in particular circuit, which is produced using a polymer mixture as claimed in any of claims 1 to 7 and/or has a double layer as claimed in claim 9.